

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Bernstein et al.

Conf. No.: 4327

Serial No.: 10/710,641

Art Unit: 2819

Filing Date: 07/27/2004

Examiner: White, Dylan C.

Title: ERROR CORRECTING LOGIC
SYSTEM

Docket No.: BUR920020035US1
(IBMB-0021)

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

AMENDMENT UNDER 37 CFR §1.111

Sir:

I. INTRODUCTION:

In response to the Office Action mailed on June 12, 2007, Applicants propose to amend the above-referenced patent application as follows:

Amendments to the Drawings are reflected in the explanation of amendment to drawings that begins on page 2 of this paper.

Amendments to the Claims are reflected in the listing of claims that begins on page 3 of this paper.

Remarks begin on page 5 of this paper.